L	Hits	Search Text	DB	Time stamp
Number	1000		TIGDAM.	2004/11/00
1	1266	<pre>(random adj access adj memory) and (thread\$3 and engine\$1)</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08
2	216	((random adj access adj memory) and (thread\$3 and engine\$1)) and (memory adj controller)	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08
3	331	((random adj access adj memory) and (thread\$3 and engine\$1)) and (multi adj thread\$3)	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08 16:28
4	40	<pre>(((random adj access adj memory) and (thread\$3 and engine\$1)) and (memory adj controller)) and (((random adj access adj memory) and (thread\$3 and engine\$1)) and (multi adj thread\$3))</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08
5	5		USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08
6	6	(threaded adj engine\$1) and RAM	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08 16:33
8	3	(RAM and (thread\$3 adj engine\$1)) and controller	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08 16:34
7	10	RAM and (thread\$3 adj engine\$1)	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08 16:35
9	577	RAM and (bit adj wise)	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08 16:35
10	47	(RAM and (bit adj wise)) and thread\$3	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08 16:35
11	10	((RAM and (bit adj wise)) and thread\$3) and engine\$1	USPAT; EPO; JPO; DERWENT; USOCR	2004/11/08